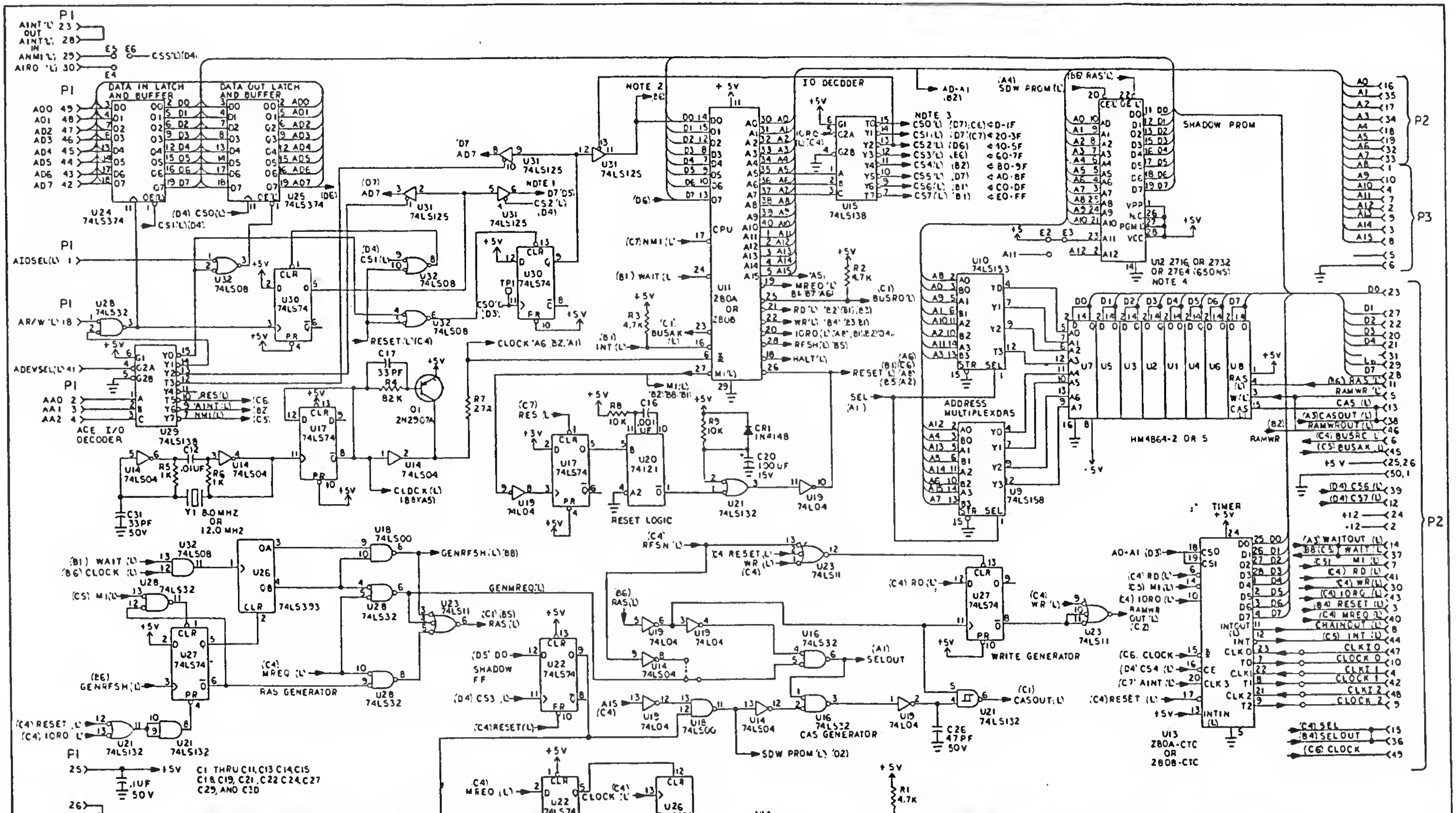
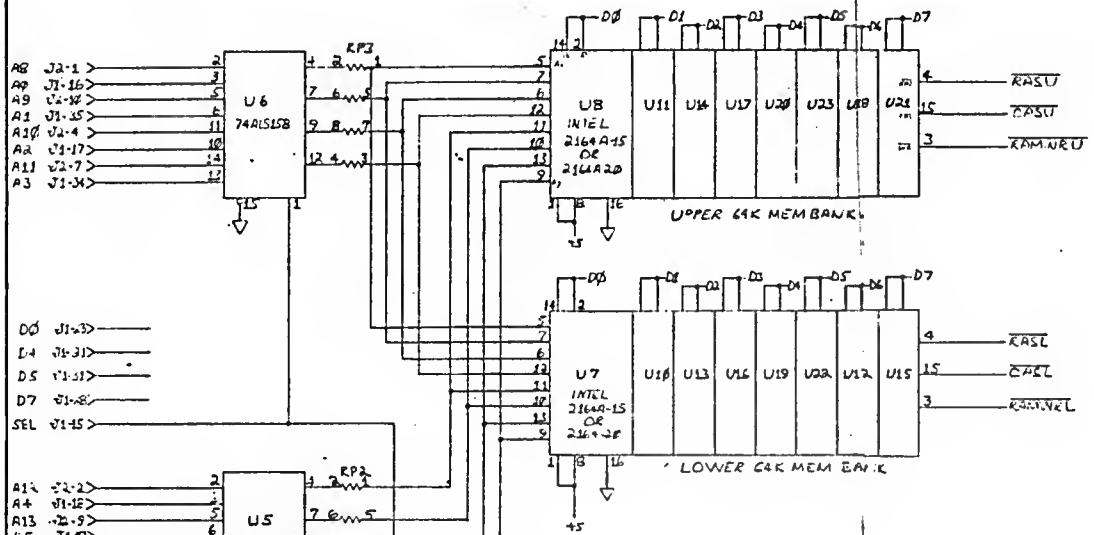


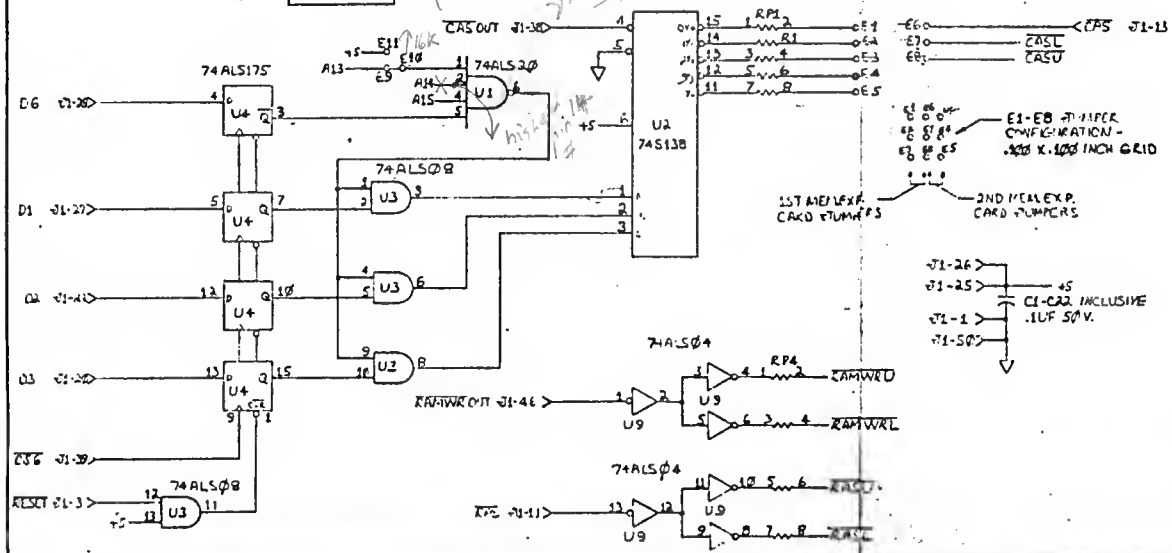
NOTE 1: D7=1 WHEN DATA TO Z80 IS READY
NOTE 2: D0=1 WHEN DATA TO APPLE IS READY
NOTE 3: CS0(L)=WRITE DATA FROM Z80 TO APPLE
CS1(L)=READ DATA FROM APPLE TO Z80
CS2(L)=READ APPLE INTR. STATUS
NOTE 4: PIN 1 OF 2716 AND 2732, CONNECT TO PIN 3 OF 28 PIN PACKAGE

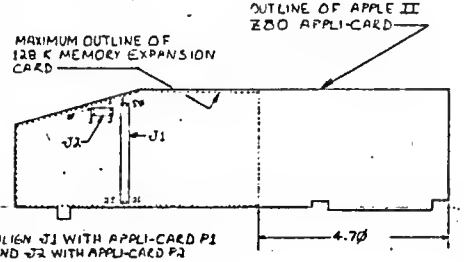
Personal Computer Products, Inc.	TITLE APPLI-CARD		
MFG. APP'D.	CHKD. BY:	DWG. NO. 7039012	REV. A
DATE 2-28-83	ENG.	DRN. BY:	SHEET 1 OF 1
SCALE —	2-28-83		

NOTE: ALL RESISTORS ARE 22Ω 1/4 WATT UNLESS OTHERWISE SPECIFIED

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
Ø	INITIAL DESIGN AND PC BOARD	10-17-82	R.K.
1	FIRST PROD. BOARDS - JUMPER CONFIG CHANGE	12-29-82	R.K.







NOTES:

1- PATED HOLES FOR JUMPERS E1-E11 AND CONNECTORS J1 AND J2 ARE .040 INCH

- DIMENSIONS ARE INCHES

APR 06 1983

TOLERANCES UNLESS OTHERWISE SPECIFIED FRACTIONS DEC ANGLES ± .005	Personal Computer Products, Inc.	
APPROVALS / DRAWN: RAY KLEIN 3-17-82 / CHECKED	APPLE II 280 APPLI-CARD 128 K MEMORY EXPANSION BOARD	
	SCALE 1:2	SIZE C
	DRAWING NO 001-0002-01	
	DO NOT SCALE DRAWING	SHEET 1 OF 1